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Extraction of series resistance and mobility degradation parameter in MOSFETs using iterative method

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ABSTRACT

Series resistance and mobility attenuation parameter are parasitic phenomena that limit the scaling of advanced MOSFETs. In this work, an iterative method is proposed to extract the series resistance and mobility degradation parameter in short channel MOSFETs. It also allows us to extract the surface roughness amplitude. The principle of this method is based on the exponential model of effective mobility and the least squares methods. From these, two analytical equations are obtained to determine the series resistance and the low field mobility as function of the mobility degradation. The mobility attenuation parameter is extracted using an iterative procedure to minimize the root means squared error (RMSE) value. The results obtained by this technique for a single short channel device have shown the good agreement with measurements data at strong inversion.

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1. INTRODUCTION

Mobility degradation phenomena and parasitic source-and-drain series resistance are two parameters of special importance for MOSFET characterization. These two parameters limit scaling of advanced devices and produce effects on the device's transfer drain current-gate voltage characteristics. Accurate modeling and extraction of these parameters have a most importance for MOSFET.

According the literature, several methods and procedures have been proposed to extract the series resistance and the mobility degradation parameters [1-15]. Another methods were proposed to extract these parameters using a novel test structures, such as the multifinger MOSFET [16-17], the FinFET [18] and the amorphous MOSFET [19-20]. Recently, in works [21], a review of DC extraction methods for MOSFET parasitic series resistance and mobility degradation model parameters has been presented. In the previous work, Ortiz et al have presented and critically compared a total of eighteen extraction procedures; some of these methods use several devices with different mask channel lengths and other methods need a single device but under different drain biases. Rodriguez-Davila et al proposed a new integration-basesed procedure to extract the mobility enhancement factor, and the series resistance of thin- film MOSFETs [22].

In this paper we present an iterative method based on exponential model of mobility and analytical approach using least squares method. We formulate two analytical expressions as function of the mobility attenuation parameter. This parameter is then extracted iteratively at the minimum of the root means squared

error. The advantage of this method that is used only a single test device and one drain biase and alow us also to extract the surface roughness amplitude.

The paper is organized as follows: Section 2 presents the modeling of the drain current using the model of effective mobility and we formulate the extraction technique. The validation of the proposed method using experiment data of single device is shown in section 3. Finally, the paper is closed by the conclusion of section 4.

2. MODELING AND EXTRACTION TECHNIQUE

2.1. Drain current modeling

The drain current I_d, at very small drain bias, can be expressed in terms of intrinsic voltages as:

$$I_{d} = \frac{W}{L} \mu_{eff} C_{ox} \left(V_{G} - V_{t} \right) V_{D}$$
(1)

Where W is the channel width, L is the channel length, Cox is the oxide capacitance, μ_{eff} is the effective free-carrier mobility, V_G is the intrinsic gate voltage, V_D the intrinsic drain voltage and V_t is the threshold voltage. If the source-and-drain series resistance is significant, the device's intrinsic gate and drain voltages are:

$$V_{G} = V_{g} - I_{d} \cdot \frac{R_{sd}}{2} \tag{2}$$

$$V_{D} = V_{d} - I_{d}.R_{sd}$$
(3)

Where V_g and V_d are the externally applied gate and drain voltages, respectively, R_{sd} is the total source-and-drain series resistance as shown in Figure 1.

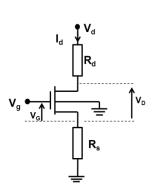


Figure 1. Intrinsic and externally applied voltages in MOSFET

The model of variation mobility with effective field considers that the attenuation of effective mobility is particularly due to Surface Roughness Scattering [4]. This work generalized all the classical models [4, 10, 12], and provided a physical meaning to different used parameters.

The effective mobility is given by the exponential model in strong inversion [4, 12]:

$$\mu_{\text{eff}} = \mu_0 \cdot \exp(-\theta(V_G - V_t)) \tag{4}$$

Where,

$$\theta = \frac{\eta \beta \Delta . C_{ox}}{\varepsilon_{si}} \tag{5}$$

 θ is the intrinsic attenuation coefficient of mobility, η is constant parameter equal to 0.5 for electrons and 0.33 for holes [4], $\beta = q/kT$ is the inverse of the thermal potential, ϵ_{si} silicon permittivity, Δ is the surface roughness amplitude, μ_0 represents the low-field mobility.

Assuming that $V_g - V_t >> I_d.R_{sd}/2$, and substitution of (4) in (1) yields:

$$I_{d} = K. \frac{V_{g} - V_{t}}{\exp(\theta(V_{g} - V_{t})) + \frac{K.R_{sd}}{V_{d}}(V_{g} - V_{t})}$$

$$(6)$$

Where,

$$K = \frac{W}{L} \cdot \mu_0 \cdot C_{ox} \cdot V_d \tag{7}$$

2.2. Extraction method

The procedure is based on calculating the function $R(V_g)$:

$$R(V_{g}) = \frac{V_{g} - V_{t}}{I_{d}} = \frac{V_{gt}}{I_{d}} = \frac{1}{K} \cdot exp(\theta V_{gt}) + \frac{R_{sd}}{V_{d}} \cdot V_{gt}$$
(8)

The problem consists to minimize the follow objective function:

$$S = \sum_{i=1}^{N} (R_{i,th} (V_{gti}, K, R_{sd}) - R_{i,exp})^{2}$$
(9)

 $I_{i,cal}$ is the theoretical characteristic that can be rewritten using the (1):

$$R_{i,th}\left(V_{gti}\right) = \frac{1}{K}.EXP_i + \frac{R_{sd}}{V_d}.V_{gti}$$
(10)

Where EXPi is given by:

$$EXP_{i} = exp(\theta.V_{gti})$$
(11)

 $R_{i,exp}$ and V_{gti} are the measured R and voltage at the ith point and N is the number of measured data points. For minimizing the objective function S, we must solve the following system:

$$\begin{cases} \frac{\partial S}{\partial (1/K)} = 2 \sum_{i=1}^{N} \frac{\partial R_{i,th}}{\partial (1/K)} \left(R_{i,th} - R_{i,exp} \right) = 0 \\ \frac{\partial S}{\partial R_{sd}} = 2 \sum_{i=1}^{N} \frac{\partial R_{i,th}}{\partial R_{sd}} \left(R_{i,th} - R_{i,exp} \right) = 0 \end{cases}$$
(12)

After some mathematical manipulations the above system of three equations is expressed as:

$$\begin{cases} \frac{1}{K} \sum_{i=1}^{N} EXP_{i}^{2} + \frac{R_{sd}}{V_{d}} \sum_{i=1}^{N} EXP_{i}.V_{gti} = \sum_{i=1}^{N} EXP_{i}R_{i,exp} \\ \frac{1}{K} \sum_{i=1}^{N} EXP_{i}.V_{gti} + \frac{R_{sd}}{V_{d}} \sum_{i=1}^{N} V_{gti}^{2} = \sum_{i=1}^{N} R_{i,exp}V_{gti} \end{cases}$$
(13)

After resolving the previous system, the expressions of K and Rsd as function of θ are given by:

Where a, b, c, d and e are given by:

$$\begin{split} a &= \sum_{i=1}^{N} EXP_{i}^{2}, b = \sum_{i=1}^{N} EXP_{i}.V_{gti}; c = \frac{R_{sd}}{V_{d}} \sum_{i=1}^{N} V_{gti}^{2} \\ d &= \sum_{i=1}^{N} EXP_{i}R_{i,exp}; e = \sum_{i=1}^{N} R_{i,exp}V_{gti} \end{split} \tag{15}$$

With the aim of the extraction of parameter θ , we use an iterative technique which fond the optimal value of θ at the minimum of the RMSE:

RMSE =
$$\sqrt{\frac{1}{N} \sum_{i=1}^{N} (R_{i,exp} - R_{i,th})^2}$$
 (16)

The algorithm of the proposed method to extract the series resistance, the mobility attenuation parameter is summarized in the Figure 2.

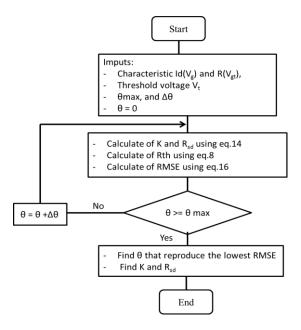


Figure 2. Flow chart of calculating parameters

3. RESULTS AND DISCUSSION

In order to validate the presented method, the MOSFET transistor measured in this work has the following parameters: channel width $W=4\mu m$, gate oxide thickness tox = 5nm, channel length $L=0.1\mu m$, and channel doping $Na=10^{16}$ cm⁻³. The threshold voltage of this device is $V_t=0.2V$. The proposed method was implemented in MATLAB.

Table 1 shows the extracted parameters using the proposed method obtained for this device and compared with two methods used the same device and the same exponential model of mobility. As it can be seen, the results obtained using the proposed method, have a good agreement with those previously published in the two works. The surface roughness amplitude is extracted using the (5).

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Table 1. Extraction parameters using the proposed method			
Parameter	$R_{sd}(\Omega)$	$\theta(V^{-1})$	Δ (nm)
Proposed method	108.82	0.36	0.28
Method in [10]	108.80	0.36	0.28
Method in [4]	107.53	0.36	0.31

Figure 3 shows the plot of the RMSE versus the mobility degradation parameter, the value of $\Delta\theta$ used in this algorithme equal to 0.01. As it can be seen, the optimal value of θ that reproduce the lowest value of RMSE = 2.403 is 0.36 V⁻¹.

Figure 4 shows the experimental characteristic and the theoretical one of the resistance R versus Vgt at ohmic regime. The theoretical values of R are calculated by injecting the values of the extracted parameters using our method in the (8). It is clear that the calculated curve using the obtained parameters fit the experimental curve with accuracy.

The experimental characteristic of the drain current versus V_g and the theoretical curve calculated from the extracted parameters and using the (6) are plotted in Figure 5. The application of the presented gives a best fit with experimental data in ohmic regime.

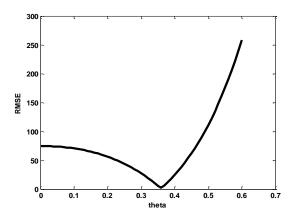
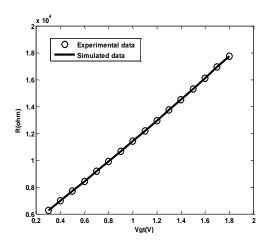


Figure 3. Plot of RMSE vs. theta



0.8 0.4 0.2 0.5 1 1.5 2

Experimental dat

Simulated data

Figure 4. Plot of experimental and theoretical characteristics of R vs. Vgt

Figure 5. Plot of experimental and theoretical Id vs. Vg characteristics

4. CONCLUSION

In this paper, an iterative method for the extraction of the parasitic series resistance and the mobility attenuation parameters in MOSFET transistors was presented. This technique is based on the analytical expression of the series resistance and the mobility at low field using linear least squares method and solving

a system of two equations as function mobility attenuation parameter. The mobility degradation parameter is determined iteratively as the one, that minimizes the RMSE value. The validation of this method using a single device shows a good agreement with experimental data. The obtained parameter values have been also compared with the parameters obtained for the same device using other methods available in the literature. Consistency between the results has been demonstrated.

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